Notice of Allowability	Application No.	Applicant(s)
	10/791,500	FUJIYOSHI ET AL.
	Examiner	Art Unit
	Adolf Berhane	2838
The MAILING DATE of this communication appears on the cover sheet with the correspondence address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.		
1. This communication is responsive to <u>papers filed on 3/2/04.</u>		
2. The allowed claim(s) is/are <u>1-7</u> .		
3. X The drawings filed on <u>02 March 2004</u> are accepted by the Examiner.		
4.		
 Attachment(s) 1. ☑ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948) 3. ☑ Information Disclosure Statements (PTO-1449 or PTO/SB/0 Paper No./Mail Date 3/2/04 4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material 	5. Notice of Informal P 6. Interview Summary Paper No./Mail Dat 8), 7. Examiner's Amendo 8. Examiler's Stateme 9. Other	ratent Application (PTO-152) (PTO-413), te
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- 1. Claims 1-7 are allowed over the cited prior art.
- 2. The following is an examiner's statement of reasons for allowance: None of the cited prior art disclose or teach a power supply with a first regenerative circuit being connected between the junction of the first snubber capacitor and the first snubber unidirectionally conducting device and the DC power supply, and including first voltage inducing means for converting a voltage across the load circuit and supplying a converted voltage to the first regenerative circuit when the first semiconductor switching device is conductive; the second regenerative circuit being connected between the junction of the second snubber capacitor and the second snubber unidirectionally conducting device and the DC power supply, and including second voltage inducing means for converting a voltage across the load circuit and supplying a converted voltage to the second regenerative circuit when the second semiconductor switching device is conductive.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Adolf Berhane whose telephone number is 571-272-2077. The examiner can normally be reached on Monday- Friday 8 AM to 6 PM.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Michael Sherry can be reached on 571-272-2084. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Adolf Berhane Primary Examiner Art Unit 2838